

FIG .4

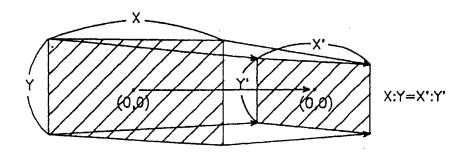


FIG .5

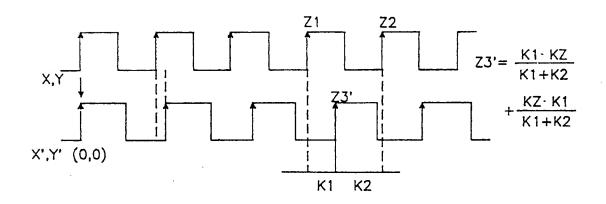
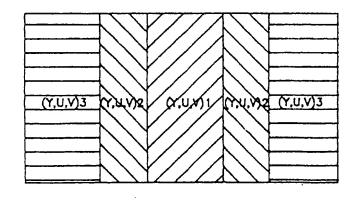
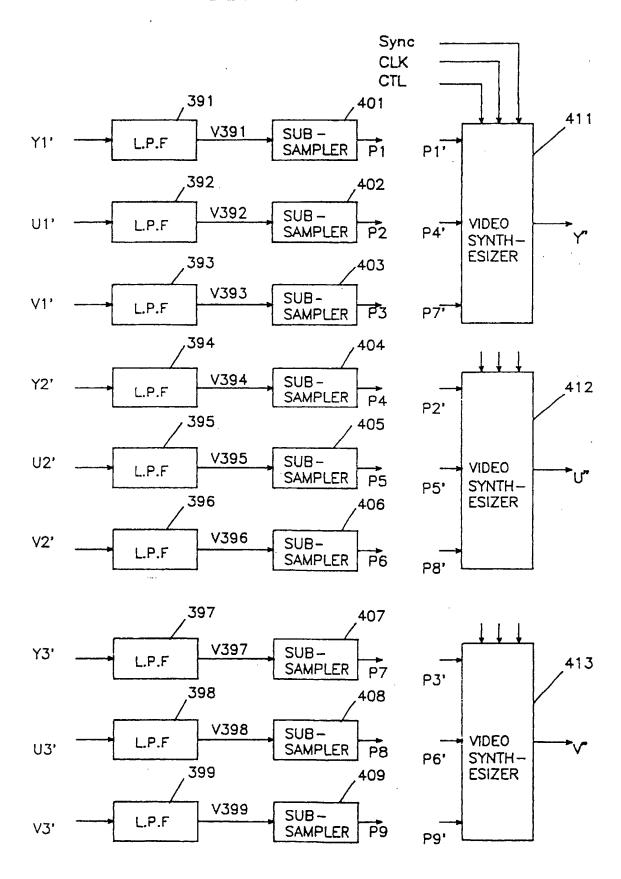


FIG .6



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FIG .7



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FIG .8

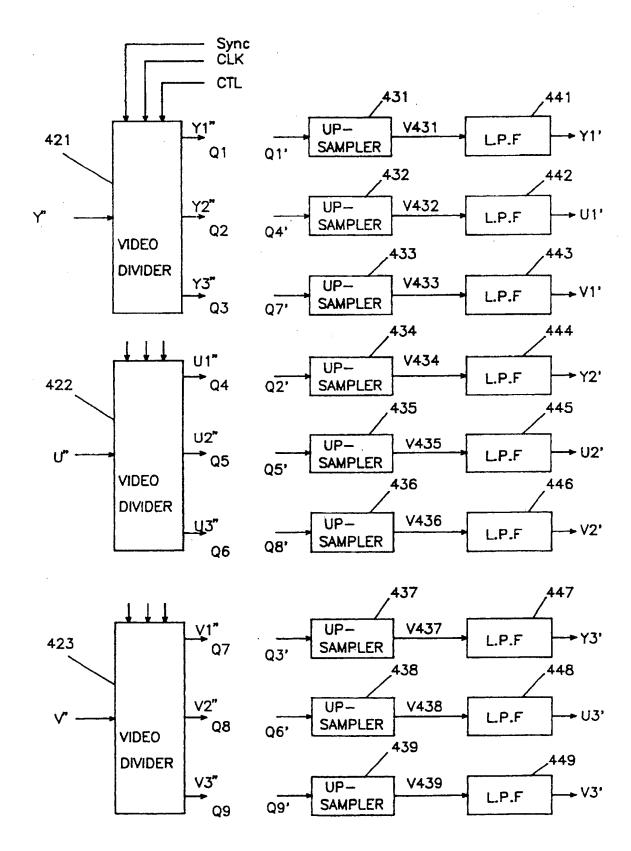
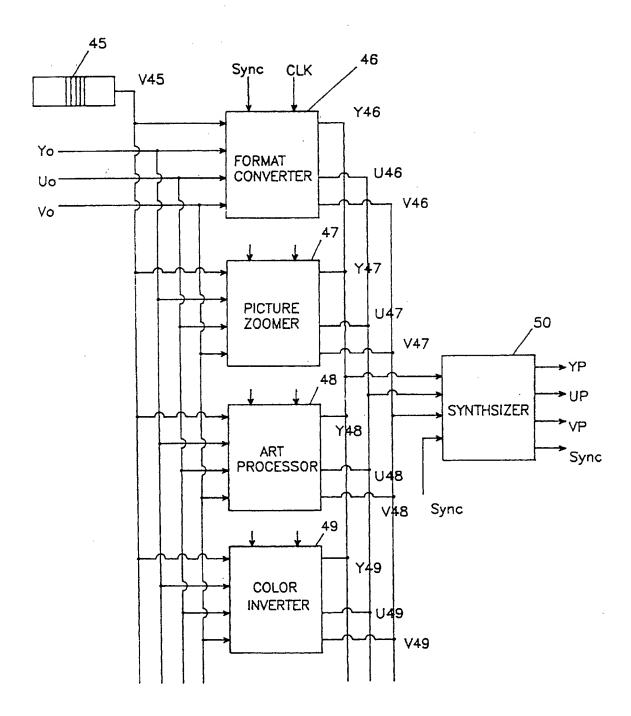


FIG .9



19 D.C.T 65 **V57** ACTIVITY CALCULATOR DELAY 57 SCRAMBLE MAP TABLE SCRAMBLER **V65** 63 **V**56 CONTROL PARAMETER CAL DATA SELECTOR SCRAMBLE CONTROLLER V59 FRAME BUFFER 68 766 64 SCRAMBLE PROTECTION KEY PARAMETER INITIALIZER CODING H.V.S I/P FORMA-TTER 55 Vsf 67 **V53** QUANT-18 FIELD MEMORY FIELD MEMORY BUFFER 69 Vcd Vcd 69/ 53 53 FIELD BASED PO. 2 FORMA-TTER > FRAME BASED V.LC TABLE 270 SW3 BUFFER 3 ₹ 500 009

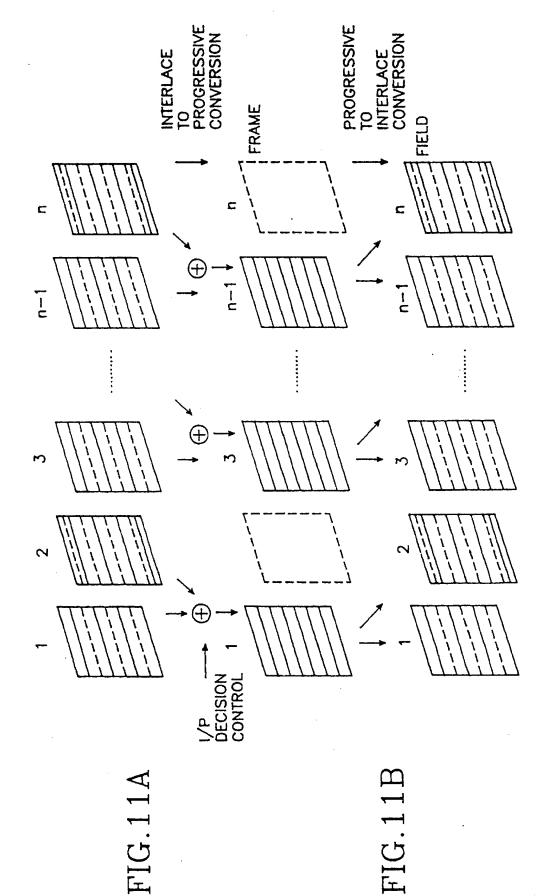
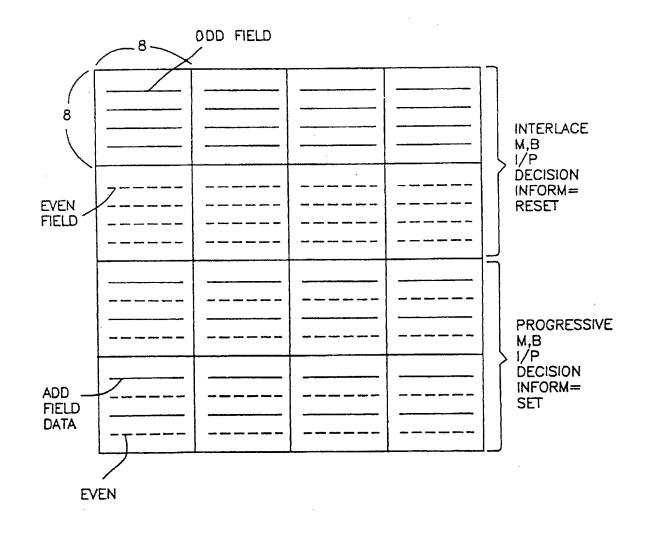
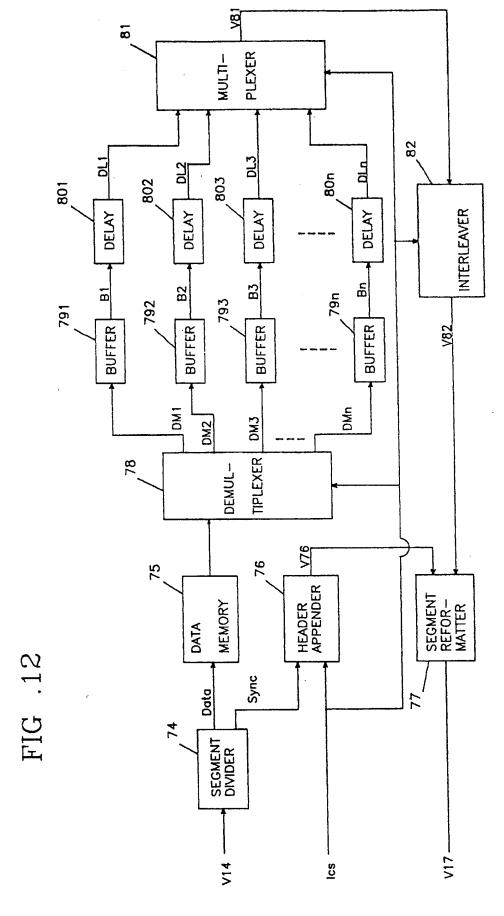


FIG.11C





U.S. Patent

FIG.13A

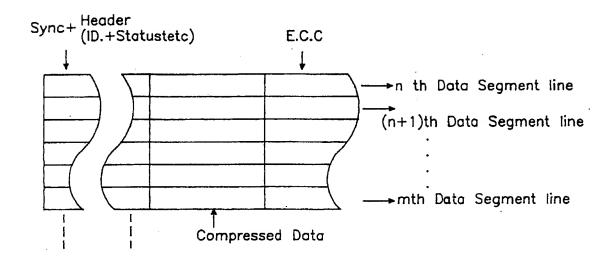


FIG.13B

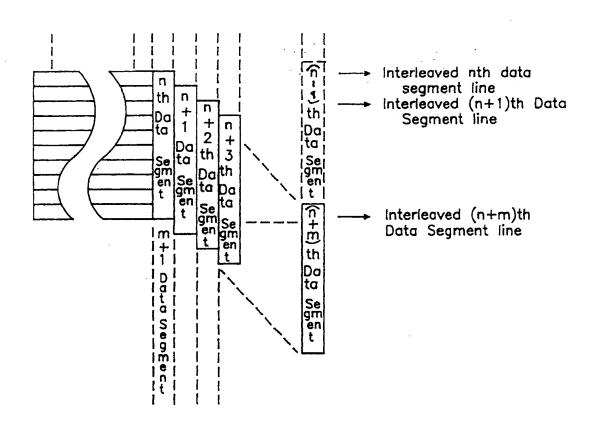
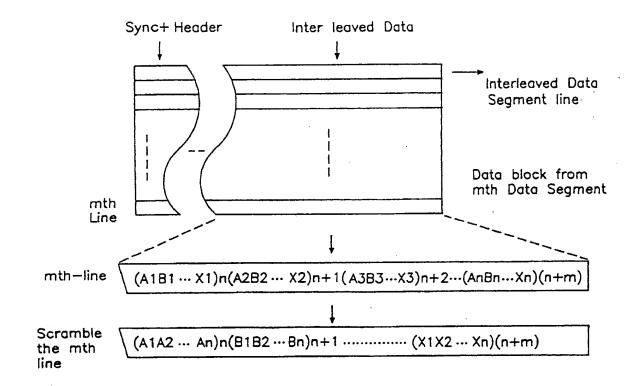




FIG.13C



An,Bn,... Xn : Data Symbol(Composed & Bite)

(..........)n : Data Block from n—th Data Segment

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